



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Brown, et al. )  
Confirmation No.: 3628 )  
Filed: January 28, 2002 )  
Group Art Unit: 2836 )  
Serial No.: 10/058,863 )  
Examiner: Deberadinis, Robert L. )  
Title: Real-Time Digital Voltage )  
Sag Compensator )  
Atty Docket No.: ATA-5 )

**SUBMISSION OF FORMAL DRAWINGS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith are five sheets of formal drawings (Figs. 1-5) to be substituted for the informal drawings presently on file in the above-identified application. The Official Draftsperson is respectfully requested to approve these drawings for entry into the application.

Respectfully submitted,

Kareem M. Irfan  
Registration No. 32,326  
Attorney for Applicant

Square D Company/Schneider Electric  
Legal Dept. - I.P. Group  
1415 S. Roselle Road  
Palatine, IL 60067  
Phone: 847-925-3458 (or 3217)  
Fax: 847-925-7419

**CERTIFICATE UNDER 37 CFR 1.8**

I hereby certify that this document is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to: Commissioner of Patents, POB 1450, Alexandria, VA 22313-1450, Mail Stop Issue Fee, on this 14th day of August, 2006.

  
Laura G. Weiss